

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1. – 12. (Cancelled)

13. (Currently Amended) An alarm unit, comprising:
a flash circuit having a flashtube for generating a flash;
an application specific integrated circuit (ASIC) coupled to said flash circuit, for triggering said flash; and
an audio circuit having at least one of: a horn or a buzzer, coupled to said ASIC, where said audio circuit generates an audio warning signal, wherein said ASIC selects an audio frequency for said audio warning signal, wherein said audio frequency being a sweep frequency of approximately 2500 Hertz (Hz) to 4000 Hz; and
a sync pulse detection circuit coupled to said ASIC for detecting a sync pulse, wherein the sync pulse is detected if a voltage drops to a logic low on a pin of the ASIC for greater than a predetermined time period, wherein the detecting of the sync pulse causes the flashtube to generate the flash and causes the horn or the buzzer to generate the audio warning signal in a code 3 pattern, wherein the code 3 pattern comprises an approximately 0.5 second period of silence and an approximately 0.5 second period of sound repeated three times followed by an approximately 1.0 second period of silence.

Claims 14. – 15. (Cancelled)

16. (Currently Amended) An alarm unit, comprising:
a flash circuit having a strobe for generating a flash;
an audio circuit having at least one of: a horn or a buzzer for generating an audio warning signal; and

an application specific integrated circuit (ASIC) coupled to said flash circuit, and to said audio circuit, for triggering said audio warning signal, wherein said ASIC selects an audio frequency for said audio warning signal, wherein said audio frequency being a sweep frequency of approximately 2500 Hertz (Hz) to 4000 Hz, and

a sync pulse detection circuit coupled to said ASIC for detecting a sync pulse, wherein the sync pulse is detected if a voltage drops to a logic low on a pin of the ASIC for greater than a predetermined time period, wherein the detecting of the sync pulse causes the strobe to generate the flash and causes the horn or the buzzer to generate the audio warning signal in a code 3 pattern, wherein the code 3 pattern comprises an approximately 0.5 second period of silence and an approximately 0.5 second period of sound repeated three times followed by an approximately 1.0 second period of silence.

17. (Canceled)

18. (Previously Presented) The alarm unit of claim 13, wherein said flash circuit further comprises a voltage doubler.

19. (Previously Presented) The alarm unit of claim 13, wherein said ASIC provides a charge cycle that is greater than 8 kilohertz.